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000026021 7590 07/01/2005

**HOGAN & HARTSON L.L.P.
500 S. GRAND AVENUE
SUITE 1900**

10/04/2005 CCHAUZ 06000018 10662780

01 FC:1501 1400.00 OP
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Farroze Vakilzadeh (Depositor's name)
Farroze Vakilzadeh (Signature)
September 30, 2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/662,780 09/15/2003 Kazuko Inuzuka 81790.0296 1491

TITLE OF INVENTION: SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE OF FAST RANDOM CYCLE SYSTEM AND TEST METHOD THEREOF

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional NO \$1400 \$300 \$1700 10/03/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
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PHAM, LY D 2827 365-191000

01 FC:1501 1400.00 OP
02 FC:8001 9.00 OP
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1 **Hogan & Hartson, LLP**
2
3

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Kabushiki Kaisha Toshiba

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature **[Signature]**
Typed or printed name **TRAY SCHWILKE**

Date **9/30/05**
Registration No. **34,127**

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